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LSI 01-309 PATENT

CLAIMS

What is claimed is:

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- 2 (a) decoding a received data block to form a reconstructed data block;
- 3 (b) determining a column parity of each column of said reconstructed data block;
- 4 (c) producing a list of most probable errors and an associated metric for relating to 5 each row of said reconstructed data block;
- 6 (d) creating an error mask utilizing said list of most probable errors; and
- performing an exact match function for each row of said reconstructed data block;
 wherein a result of said exact match function and said associated metric are
 compared to determine if a correction of a bit of said reconstructed data block is
 made for each row.
 - 2. The method as claimed in claim 1, wherein decoding of said received data block includes transfer of said received data block through a Viterbi detector.
- The method as claimed in claim 1, wherein said producing of said list of
 most probable errors and said associated metric is accomplished by sending said
 reconstructed data block through a set of matched filters.
- 1 4. The method as claimed in claim 1, wherein determining of said column 2 parity of each column of said reconstructed data block creates a column parity check 3 syndrome.
- The method as claimed in claim 4, wherein said error mask defines a vicinity of said column parity check syndrome for which said list of most probable errors should be checked against.

1 6. The method as claimed in claim 4, wherein calculating of said exact 2 match function comprises:

- performing an exclusive OR operation with said list of most probable errors and said column parity check syndrome to create a first result; and
- 5 (b) performing an AND operation with said first result and said error mask to create a 6 second result, wherein said second result is a result of said exact match function.
- The method as claimed in claim 1, wherein a priority of correction is given to a row in which said result of said exact match function is an exact match.

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1 Χ	A method	of accurately	correcting da	ta; comprising;
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- 2 (a) decoding a received data block to form a reconstructed data block;
- determining a column parity of each column of said reconstructed data block to form a column parity check syndrome;
- 5 (c) producing a list of most probable errors and an associated metric for relating to 6 each row of said reconstructed data block;
- 7 (d) creating an error mask utilizing said list of most probable errors;
- 8 (e) performing an exclusive OR operation with said list of most probable errors and 9 said column parity check syndrome to create a first result; and
- performing an AND operation with said first result and said error mask to create a second result, wherein said second result is a result of an exact match function, said result of said exact match function and said associated metric being compared to determine if a correction of a bit of said reconstructed data block is made for each row.
 - 9. The method as claimed in claim 8, wherein decoding of said received data block includes transfer of said received data block through a Viterbi detector.
- 1 10. The method as claimed in claim 8, wherein said producing of said list of
 2 most probable errors and said associated metric is accomplished by sending said
 3 reconstructed data block through a set of matched filters.
- 1 11. The method as claimed in claim 8, wherein said error mask defines a vicinity of said column parity check syndrome for which said list of most probable errors should be checked against.

1 12. The method as claimed in claim 8, wherein a priority of correction is given

2 to a row in which said result of said exact match function is an exact match.

1 17	A digital	nroceccing system	comprising
1 13.	A digital	processing system,	, comprising

- 2 (a) means for decoding a received data block to form a reconstructed data block;
- 3 (b) means for producing a list of most probable errors and an associated metric
- relating to each row of said reconstructed data block; said producing means being
- 5 coupled to said decoding means;
- 6 (c) means for determining a column parity of said reconstructed data block to form a column parity check syndrome;
- 8 (d) means for creating an error mask coupled to said producing means; and
- 9 (e) means for performing an exact match function to produce a result; said means for
- performing an exact match being coupled to said means for determining a column
- parity and said means for producing a list of most probable errors and an
- 12 associated metric; and
- 13 (f) means for correcting data of said reconstructed data block; wherein said correcting
- means compares said result of said exact match function and said associated
- metric to determine if a correction of a bit of a row of said reconstructed data
- block is made.
- 1 14. The digital signal processing system as claimed in claim 13, wherein said
- 2 decoding means includes a Viterbi detector.
- 15. The digital signal processing system as claimed in claim 13, wherein said
- 2 producing means includes a set of matched filters which receives said reconstructed data
- 3 block.
- 1 16. The digital signal processing system as claimed in claim 13, wherein said
- 2 error mask defines a vicinity of said column parity check syndrome for which said list of
- 3 most probable errors should be checked against.

- 1 The digital signal processing system as claimed in claim 13, wherein said
- 2 exact match function includes an exclusive OR operation with said list of most probable
- 3 errors and said column parity check syndrome and an AND operation of a result of said
- 4 exclusive OR operation with said error mask.
- 18. The digital signal processing system as claimed in claim 13, wherein a
 - priority of correction is given to a row in which said result of said exact match function is
- 3 an exact match.

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